



2815

Dkt. 2271/60617

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of : Yoshinori UEDA

Serial No. : 09/431,593

Group Art Unit: 2815

Date Filed : November 1, 1999

Examiner: M. E. Warren

For : SEMICONDUCTOR DEVICE HAVING AN
INTEGRAL RESISTANCE ELEMENT#5
Election
J. McVie
5/18/011185 Avenue of the Americas
New York, N.Y. 10036Assistant Commissioner for Patents
Washington, D.C. 20231RESPONSE TO CLAIM RESTRICTION REQUIREMENTRECEIVED
MAY 17 2001
TECHNOLOGY CENTER 2000

In response to the Office Action dated April 24, 2001 requiring restriction of inventions under 35 U.S.C. 121, applicant hereby elects, with traverse, to prosecute Group I, claims 1-8.

The restriction requirement is traversed because it is believed that all of the claimed inventions are sufficiently related to each other to be examined in one patent application.

Respectfully submitted,

RICHARD F. JAWORSKI
Registration No. 33,515
Attorney for Applicant
Cooper & Dunham LLP
Tel.: (212) 278-0400

I hereby certify that this paper is being deposited this date with the U.S. Postal Service as first class mail addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Richard F. Jaworski
Reg. No. 33,515Date
May 10, 2001